



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hideaki Kuwabara et al. Art Unit : 2826
Serial No. : 10/735,767 Examiner : Thomas Dickey
Filed : December 16, 2003 Confirmation No.: 8297
Title : SEMICONDUCTOR DEVICE AND METHOD FOR MANUFACTURING THE
SAME

MAIL STOP AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicants request consideration of the references listed on the attached PTO-1449 form. Under 37 C.F.R. § 1.98 (a)(2)(ii), only copies of foreign patent documents and/or non-patent literature are enclosed. Copies of any listed U.S. patents or U.S. patent application publications can be provided upon request.

Submitted herewith is an English translation of the following foreign language references, or portions thereof:

Desig. ID	Source
AU	JAPAN 06-244360 – English Abstract: DIALOG(R) File 347:JAPIO (2003); Accession No. 04572460
AV	JAPAN 08-186156 – English Abstract: DIALOG(R) File 347:JAPIO (2003); Accession No. 05230656
AW	JAPAN 2000-294723 – English Abstract: DIALOG(R) File 347:JAPIO (2003); Accession No. 06708891
AX	JAPAN 2002-049359 – English Abstract: DIALOG(R) File 347:JAPIO (2004); Accession No. 07180968 & Concise Statement: This reference was cited in the specification of U.S. Patent Application Serial No. 10/774,700, filed on February 10, 2004, and describes, in a semiconductor device, a structure in which a substrate that becomes a panel includes both a pixel portion and a driving circuit that are integrally formed as an internal circuit and an external circuit that is formed outside of the panel.
AY	JAPAN 3408154 – Full English Translation

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Attorney's Docket No.: 12732-189001 / US6847

This statement is being filed before the receipt of a first Office Action on the merits.

Please apply any charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: July 7, 2005

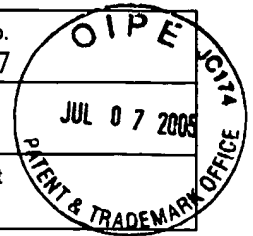


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Substitute Form PTO-1449 (Modified) Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 12732-189001	Application No. 10/735,767
	Applicant Hideaki Kuwabara et al.		
	Filing Date December 16, 2003	Group Art Unit	



U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	2002/0030189 A1	03/14/2002	Ishikawa			09/14/2001
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	AT	6,830,494 B1	12/14/2004	Yamazaki et al.			10/06/2000

Foreign Patent Documents or Published Foreign Patent Applications								
Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	AU	06-244360	09/02/1994	JAPAN			AB	
	AV	08-186156	07/16/1996	JAPAN			AB	
	AW	2000-294723	10/20/2000	JAPAN			AB	
	AX	2002-049359	02/15/2002	JAPAN			AB	

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

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	Applicant Hideaki Kuwabara et al.		
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Foreign Patent Documents or Published Foreign Patent Applications								
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							Yes	No
	AY	3408154	05/19/2003	JAPAN			Full	
	AZ	EP 0 858 110 A1	08/12/1998	EUROPE			In English	

Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
	AAA	<i>Exhibition of Active Matrix Type Organic EL Display at "13th Flat Panel Display Manufacturing Technology Expo & Conference"</i> by ELDis Group; July 2, 2003 (full translation)
	ABB	Documents distributed in the <i>"13th Flat Panel Display Manufacturing Technology Expo & Conference"</i> by ELDis Group; July 3, 2003 (full translation)
	ACC	"Mass Production of Organic EL Devices"; <i>Shimotsuke Newspaper</i> ; July 3, 2003 (full translation)
	ADD	"Two-way display developed"; Newspaper <i>"The Japan Times"</i> ; July 3, 2003
	AEE	Watanabe et al.; "Anisotropic Conductive Films for Flat Panel Displays"; <i>AM-LCD/IDW '96</i> ; pp. 369-372; November 27, 1996

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